

**GBS-9010FG (RoHS Compliant)****5V / 1310 nm / 2.125 Gb/s Single-Mode Gigabit Interface Converter (GBIC)****FEATURES**

- | Up to 2.5 Gbps Bi-directional Data Links
- | Compliant with 1X / 2X Fibre Channel FC-PI 13.0
- | Compliant with Gigabit Interface Converter (GBIC) Specification Rev. 5.5
- | 1310 nm Laser Transmitter
- | Link Distance up to 10 km
- | Hot Pluggable module with Duplex SC Connector
- | Multi-rate Operation for 2.5 / 2.125 / 1.062 / 1.25 Gbps
- | Link Distance up to 10 km
- | SCA-2 Host Connector
- | Single +5 V Power Supply
- | RoHS Compliant
- | Differential PECL Inputs and Outputs
- | TTL RX-LOS Output
- | Class 1 Laser International Safety Standard IEC 60825 Compliant

**APPLICATIONS**

- | Switch to Switch Interface
- | High Speed Interface for File Servers
- | Data Storage
- | Dual Rate 1.06 / 2.125 Gb/s Fibre Channel

**ORDER INFORMATION**

P/No.	Bit Rate (Gb/s)	FC-PI	Distance (km)	Wavelength (nm)	Package	Temp. (°C)	TX Power (dBm)	RX Sens. (dBm)	RoHS Compliant
GBS-9010F	2.125/1.063	200/100	10	1310	SC GBIC	0 to 70	-3 to -8	-21	Yes

**Absolute Maximum Ratings**

Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	Tstg	-40	85	°C	
Operating Temperature	Topr	-5	70	°C	Air flow 1m/sec
Power Supply Voltage	Vcc	-0.5	6	V	
Input Voltage	---	-0.5	Vcc	V	

**Recommended Operating Conditions**

Parameter	Symbol	Min	Typ	Max	Units	Conditions
Power Supply Voltage	Vcc	4.75	5	5.25	V	Reference to GND.
Power Supply Current	I <sub>TX</sub> + I <sub>RX</sub>		200	300	mA	
Operating Temperature	Topr	0		70	°C	Air flow 1m/sec
Data Rate			1.0625 / 1.25 2.125 / 2.5	2.5	Gb/s	
Fiber Length on 9/125 μm SMF	2			10,000	m	@1.0625 /2.125 Gb/s

<b>Transmitter Specifications (0°C &lt; Topr &lt; 70°C, 4.75 V &lt; Vcc &lt; 5.25V)</b>						
Parameter	Symbol	Min	Typ	Max	Units	Notes
<b>Optical</b>						
Optical Transmit Power	P <sub>O</sub>	-8	---	-3	dBm	1
Output Center Wavelength	λ	1285	1310	1340	nm	
Output Spectrum Width	Δλ		---	2.0	nm	RMS ( $\sigma$ )
Extinction Ratio	E <sub>R</sub>	9		---	dB	
Optical Modulation Amplitude (Peak-to-Peak)	OMA	196			μW	FC-PI Standard
Optical Rise Time	t <sub>r</sub>			150	ps	20% to 80% value
Optical Fall Time	t <sub>f</sub>			150	ps	20% to 80% value
Pout TX_Disable Asserted	P <sub>OFF</sub>			-35	dBm	
<b>Electrical</b>						
Differential Input Voltage	V <sub>IH</sub> - V <sub>IL</sub>	0.65		2.0	V <sub>p-p</sub>	
Transmit Fault Load	TX-FAULT <sub>LOAD</sub>	4.7		10	kΩ	2
Transmit Fault Output-Low	V <sub>TX-FAULT-L</sub>	0.0		0.5	V	
Transmit Fault Output-High	V <sub>TX-FAULT-H</sub>	V <sub>cc</sub> - 0.5		V <sub>cc</sub> + 0.3	V	
TX-Disable Input - Low	V <sub>TX-DISABLE-L</sub>	0		0.8	V	
TX-Disable Input - High	V <sub>TX-DISABLE-H</sub>	2.0		V <sub>cc</sub> + 0.3	V	
TX-Disable Assert Time	t <sub>off</sub>			10	μs	
TX-Disable Negate Time	t-on			1	ms	
Time to initialize, includes reset of TX-FAULT	t-int			300	ms	
TX FAULT from fault to assertion	t-fault			100	μs	
TX-Disable time to start reset	t-reset	10			μs	

1. Output power is power coupled into a 9/125 μm SM fiber.

2. Pull-up resistor on host Vcc.

<b>Receiver Specifications (0°C &lt; Topr &lt; 70°C, 4.75 V &lt; Vcc &lt; 5.25V)</b>						
Parameter	Symbol	Min	Typ	Max	Units	Notes
<b>Optical</b>						
Sensitivity @1.25Gb/s @ BER=10 <sup>-12</sup>	Sens (1X)	---	---	-22	dBm	3
Sensitivity @2.125Gb/s @ BER=10 <sup>-12</sup>	Sens (2X)			-21	dBm	3
Maximum Input Power	P <sub>in</sub>	-3		---	dBm	3
Signal detect – Hysteresis		1.0	---		dB	
Wavelength of Operation		1200	---	1600	nm	
RX-LOS – Asserted	P <sub>a</sub>	---	---	-21	dBm	Transition: low to high
RX-LOS – Deasserted	P <sub>d</sub>	-31	---	---	dBm	Transition: high to low
<b>Electrical</b>						
Differential Output Voltage	±RX-DAT	0.37		2.0	V <sub>p-p</sub>	
Data Output Rise Time	T <sub>r</sub> <sub>RX-DAT</sub>			0.35	ns	
Data Output Fall Time	T <sub>f</sub> <sub>RX-DAT</sub>			0.35	ns	
Receiver Loss of Light Load	RX-LOS <sub>LOAD</sub>	4.7		10	kΩ	4
Loss of Signal Output Voltage --- Low	RX-LOS <sub>L</sub>	0		0.5	V	
Loss of Signal Output Voltage --- High	RX-LOS <sub>H</sub>	2.4		V <sub>cc</sub>	V	
Loss of Signal Assert Time (off to on)	T <sub>A,RX-LOS</sub>			100	μs	
Loss of Signal Deassert Time (on to off)	T <sub>D,RX-LOS</sub>			100	μs	

3. Minimum sensitivity and saturation levels at BER=1E-12 for a 2<sup>7</sup>-1 PRBS.

4. Pull-up resistor on host Vcc.

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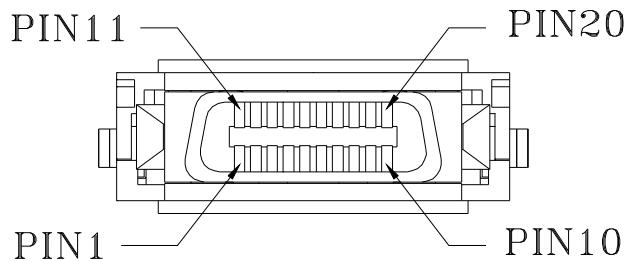
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## PIN ASSIGNMENT



PIN	Signal Name	Sequence	Description
1	RX-LOS	2	Receiver Loss of Signal, logic high, open collector compatible, 4.7K to 10K ohm pull-up to V <sub>CC</sub> T on host
2	R <sub>GND</sub>	2	Receiver Ground
3	R <sub>GND</sub>	2	Receiver Ground
4	MOD-DEF(0)	2	TTL Low
5	MOD-DEF(1)	2	SCL Serial Clock Signal
6	MOD-DEF(2)	2	SDA Serial Data Signal
7	TX-DIABLE	2	Transmitter Disable, logic high, open collector compatible
8	T <sub>GND</sub>	2	Transmitter Ground
9	T <sub>GND</sub>	2	Transmitter Ground
10	TX-FAULT	2	Transmitter Fault, logic high, open collector compatible, 4.7K to 10K ohm pull-up to V <sub>CC</sub> T on GBIC
11	R <sub>GND</sub>	1	Receiver Ground
12	-RX-DAT	1	Receiver Data Bar, Differential PECL, AC coupled
13	+RX-DAT	1	Receiver Data, Differential PECL, AC coupled
14	R <sub>GND</sub>	1	Receiver Ground
15	V <sub>CC</sub> R	2	Receiver +5V Supply
16	V <sub>CC</sub> T	2	Transmitter +5V Supply
17	T <sub>GND</sub>	1	Transmitter Ground
18	+TX-DAT	1	Transmitter Data, Differential PECL, AC coupled
19	-TX-DAT	1	Transmitter Data Bar, Differential PECL, AC coupled
20	T <sub>GND</sub>	1	Transmitter Ground

A sequence value of 1 indicates that the signal is in the first group to engage during plugging of a module. A sequence value of 2 indicates that the signal is the second and last group. The two guide pins on the connector are electrically connected to the transceiver circuit ground. These two guide pins make contact with circuit prior to sequence 1 signals.

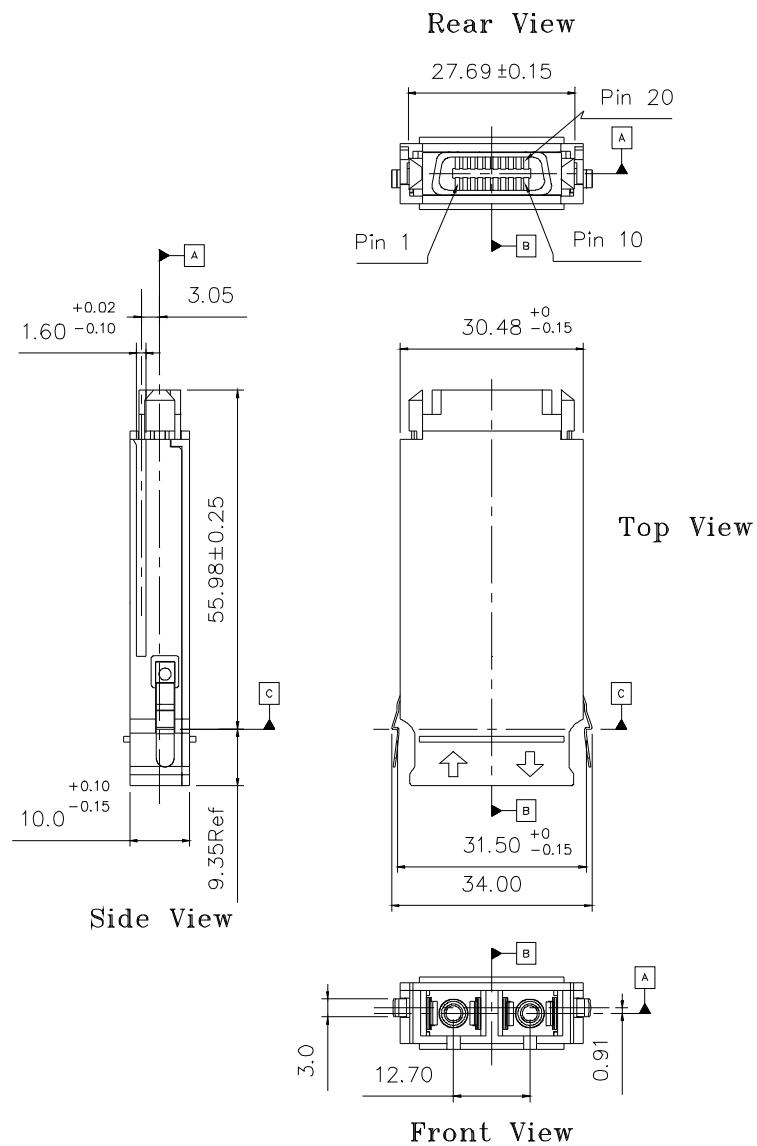
## Module Definition

Module Definition	MOD-DEF(0) PIN 4	MOD-DEF (1) PIN 5	MOD-DEF (2) PIN 6	Interpretation by Host
4	TTL Low	SCL	SDA	Serial module definition protocol

Module Definition 4 specifies a serial definition protocol. For this definition, upon power up, MOD-DEF(1:2) appear as no connector (NC) and MOD-DEF(0) is TTL LOW. When the host system detects this condition, it activates the serial protocol. The protocol uses the 2-wire serial CMOS E<sup>2</sup>PROM protocol of the ATMEL AT24C01A/02/04 family of components.

## PACKAGE DIAGRAM

Units in mm



**Note:** Specifications subject to change without notice.