TRSL-5120G / TRSL-5120AG / TRSL-5120CG / TRSL-5120ACG
3.3V / 1310 nm / 622 Mbps RoHS Compliant SFF LC SINGLE-MODE TRANSCEIVER

FEATURES

- Duplex LC Single Mode Transceiver
- SONET OC-12 IR-1 / SDH STM-4 (S-4.1) Compliant
- Small Form Factor, RJ-45 size, 2X5 pin Package
- 1310 nm LD Transmitter
- LVPECL Signal Input / Output
- LVTTL Transmitter Disable Input
- LVPECL Signal Detection Output: TRSL-5120G
- LVTTL Signal Detection Output: TRSL-5120CG
- Single +3.3 V Power Supply
- RoHS Compliant
- 0 to 70°C Operation: TRSL-5120G
- -40 to 85°C Operation: TRSL-5120AG
- Wave Solderable and Aqueous Washable
- Class 1 Laser International Safety Standard IEC-60825 Compliant

APPLICATIONS

- ATM 622 Mbps Links
- SONET / SDH Equipment Interconnect
- Fiber Channel 533 Mb/s Links

DESCRIPTION

The TRSL-5120G series single mode transceivers are small form factor, low power, high performance module for bi-directional serial optical data communications such as SONET OC-12 IR-1 / SDH STM-4 (S-4.1) and Fiber Channel. This module is designed for single mode fiber and operates at a nominal wavelength of 1310 nm. The transmitter section uses a multiple quantum well laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC. A PECL logic interface simplifies interface to external circuitry.

LASER SAFETY

This single mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

ORDER INFORMATION

P/No.	Bit Rate (Mb/s)	SONET /SDH	Distance (km)	Wavelength (nm)	Package	Temp. (°C)	TX Power (dBm)	RX Sens. (dBm)	RoHS Compliant
TRSL-5120G	622	IR-1/S-4.1	20	1310	2X5 LC	0 to 70	-8 to -14	-28	Yes
TRSL-5120AG	622	IR-1/S-4.1	20	1310	2X5 LC	-40 to 85	-8 to -14	-28	Yes

Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	Tstg	-40	85	°C	
Operating Temperature	Topr	0 -40	70 85	°C	TRSL-5120G TRSL-5120AG
Soldering Temperature	---		260	°C	10 seconds on leads only
Power Supply Voltage	Vcc	0	4.5	V	
Input Voltage	---	GND	Vcc	V	
Output Current	Iout	0	30	mA	

Recommended Operating Conditions

Parameter	Symbol	Min	Typ	Max	Units / Notes
Power Supply Voltage	Vcc	3.13	3.3	3.47	V
Operating Temperature	Topr	0 -40		70 85	°C / TRSL-5120G °C / TRSL-5120AG
Data Rate		50	622		Mb/s
Power Supply Current	Icc		180	240	mA

OPTOWAY TECHNOLOGY INC. No.38, Kuang Fu S. Road, Hu Kou, Hsin Chu Industrial Park, Hsin Chu, Taiwan 303
Tel: 886-3-5979798 Fax: 886-3-5979737
E-mail: sales@optoway.com http: // www.optoway.com

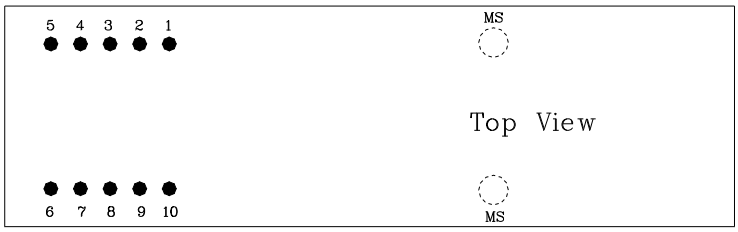
Transmitter Specifications ($0^oC < Topr < 70^oC$, 3.13V < Vcc < 3.47V)						
Parameter	**Symbol**	**Min**	**Typ**	**Max**	**Units**	**Notes**
Optical						
Optical Transmit Power	Po	-14	---	-8	dBm	1
Output Center Wavelength	λ	1274	1310	1356	nm	
Output Spectrum Width	Δλ	---	---	2.5	nm	RMS(σ)
Extinction Ratio	E_R	8.2	---	---	dB	
Output Eye	Compliant with Bellcore GR-253-CORE and ITU recommendation G.957					
Optical Rise Time	t_r			1.2	ns	10% to 90% Values
Optical Fall Time	t_f			1.2	ns	10% to 90% Values
Relative Intensity Noise	RIN			-120	dB/Hz	
Total Jiter	TJ			0.55	ns	2
Electrical						
Data Input Current – Low	I_{IL}	-350			μA	
Data Input Current – High	I_{IH}			350	μA	
Differential Input Voltage	V_{IH} - V_{IL}	300			mV	
Data Input Voltage – Low	V_{IL} - V_{CC}	-2.0		-1.58	V	3
Data Input Voltage -- High	V_{IH} - V_{CC}	-1.1		-0.74	V	3
Disable Input Voltage -- Low	$V_{TDIS,L}$	0		0.5	V	TX Output Enabled
Disable Input Voltage -- High	$V_{TDIS,H}$	Vcc – 1.3		Vcc	V	TX Output Disabled
Shut Off Time for TxDis	t_{DIS}			1	ms	

Notes: 1. Output power is power coupled into a 9/125 μm single mode fiber.
2. Measured with 2^{23}-1 PRBS with 72 ones and 72 zeros
3. These inputs are compatible with 10K, 10KH and 100K ECL and PECL inputs.

Receiver Specifications ($0^oC < Topr < 70^oC$, 3.13V < Vcc < 3.47V)						
Parameter	**Symbol**	**Min**	**Typ**	**Max**	**Units**	**Notes**
Optical						
Sensitivity	---	---	---	-28	dBm	1
Maximum Input Power	Pin	-5		---	dBm	
Signal Detect -- Asserted	Pa	---	---	-28	dBm	Transition: low to high
Signal Detect -- Deasserted	Pd	-40	---	---	dBm	Transition: high to low
Signal detect -- Hysteresis		1.0	---		dB	
Wavelength of Operation		1100	---	1600	nm	
Electrical						
Data Output Voltage – Low	V_{OL} - V_{CC}	-2.0		-1.58	V	2
Data Output Voltage – High	V_{OH} - V_{CC}	-1.1		-0.74	V	2
Signal Detect Output Voltage -- Low	V_{OL}	-2.0		-1.58	V	TRSL-5120G
Signal Detect Output Voltage -- High	V_{OH}	-1.1		-0.74	V	
Signal Detect Output Voltage -- Low	V_{OL} - V_{CC}			0.5	V	TRSL-5120CG
Signal Detect Output Voltage -- High	V_{OH} - V_{CC}	2.0			V	

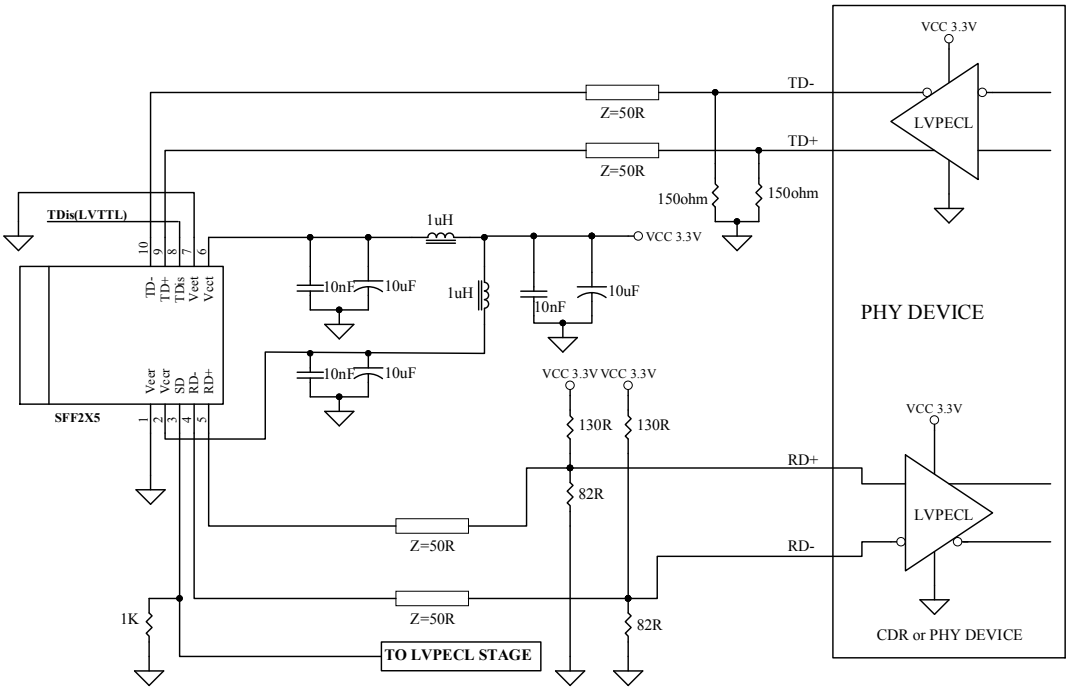
Notes: 1. Minimum sensitivity and saturation levels at BER=1E-10 for a 2^{23}-1 PRBS with 72 ones and 72 zeros.
2.These outputs are compatible with 10K, 10KH and 100K ECL and PECL outputs.

CONNECTION DIAGRAM



PIN	Symbol	Notes
1	V_{EE}r	Directly connect this pin to the receiver ground plane
2	V_{CC} r	+3.3V dc power for the receiver section
3	SD	Active high on this indicates a received optical signal.
4	RD–	Receiver Data out Bar. See recommended circuit schematic
5	RD+	Receiver Data out. See recommended circuit schematic
6	V_{CC} t	+3.3V dc power for the transmitter section
7	V_{EE}t	Directly connect this pin to the transmitter ground plane
8	TDis	Transmitter Disable. Connect this pin to +3.3V TTL logic "1" to disable module To enable module connect to TTL logic low "0"
9	TD+	Transmitter Data In. See recommended circuit schematic
10	TD-	Transmitter Data In Bar. See recommended circuit schematic
MS	MS	Mounting Studs. Connect to Chassis Ground

RECOMMENDED CIRCUIT SCHEMATIC

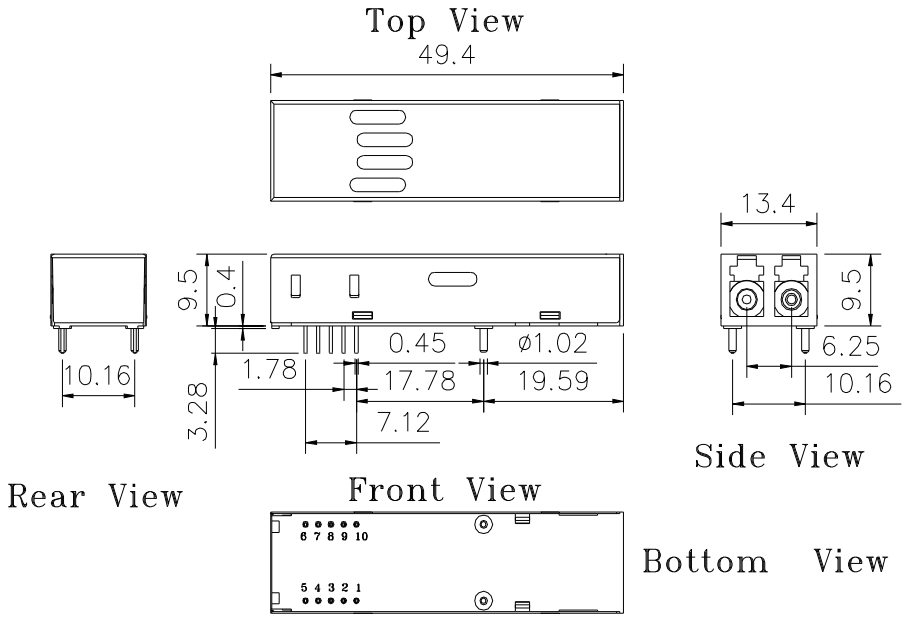


Note: 1. 1000Ω SD Output pull-down resistor required for TRSL-5120G / TRSL-5120AG (LVPECL SD Output).
2. No pull-down resistor required for TRSL-5120CG / TRSL-5120ACG (LVTTL SD Output)
3.Veer and Veet are not internally connected to each other.
4. 50 Ω line pattern and component placements on TD+/TD- and RD+/RD- lines shall be symmetrical for better impedance matching.

PACKAGE DIAGRAM

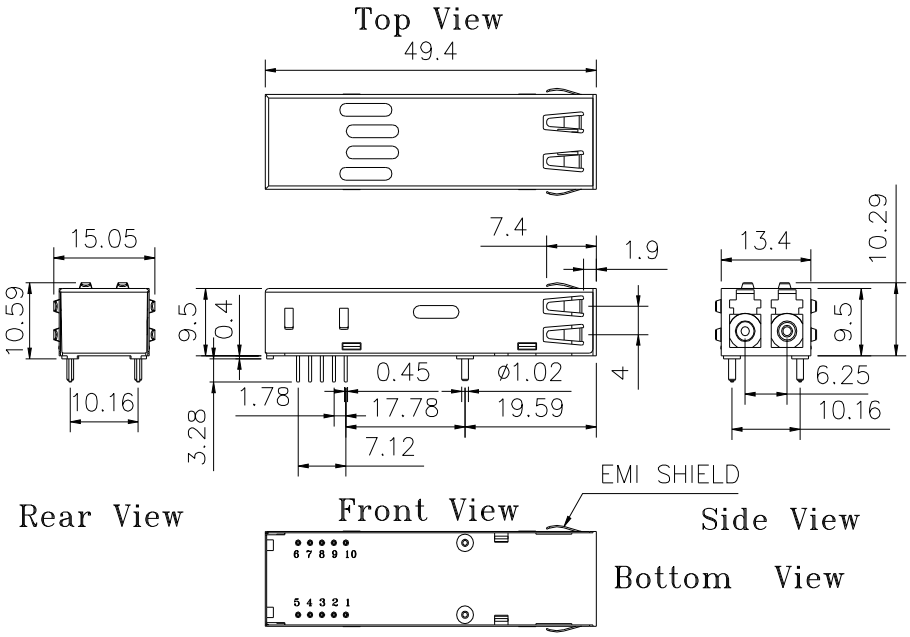
Units in mm

1) Standard Case



TRSL-5120G / TRSL-5120AG / TRSL-5120CG / TRSL-5120ACG

2) Extended Case



TRSL-5120EG / TRSL-5120AEG / TRSL-5120CEG / TRSL-5120ACEG

Note: Specifications subject to change without notice.