

TRSL-9110FG / TRSL-9110AFG

3.3V / 1310 nm / 2.125 Gbps SFF LC SINGLE-MODE TRANSCEIVER

FEATURES

- Duplex LC Single Mode Transceiver
- Fibre Channel 2X/1X SM-LC-L FC-PI Compliant
- IEEE 802.3z Gigabit Ethernet 1000BASE-LX Compliant
- Small Form Factor, RJ-45 size, 2X5 pin Package
- 1310 nm LD Transmitter
- AC/AC Coupled Signal Input / Output
- LVTTTL Transmitter Disable Input
- LVTTTL Signal Detection Output
- Single +3.3 V Power Supply
- 0 to 70°C Operating: TRSL-9110FG
- **-40 to 85°C Operating: TRSL-9110AFG**
- Wave Solderable
- Class 1 Laser International Safety Standard IEC-60825 Compliant

APPLICATIONS

- ATM Switches and Routers
- SONET / SDH Switch Infrastructure
- XDSL Applications
- Metro Edge Switching

DESCRIPTION

The TRSL-9110FG series single mode transceivers is small form factor, low power, high performance module for bi-directional serial optical data communications such as Fiber Channel 2X/1X SM-LC-L FC-PI and IEEE 802.3z Gigabit Ethernet 1000BASE-LX. This module is designed for single mode fiber and operates at a nominal wavelength of 1310 nm. The transmitter section uses a multiple quantum well laser and is a class 1 laser compliant according to International Safety Standard IEC-60825. The receiver section uses an integrated InGaAs detector preamplifier (IDP) mounted in an optical header and a limiting post-amplifier IC. A LVPECL logic interface simplifies interface to external circuitry.

LASER SAFETY

This single mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

ORDER INFORMATION

P/No.	Bit Rate (Gb/s)	FC-PI	Distance (km)	Wavelength (nm)	Package	Temp. (°C)	TX Power (dBm)	RX Sens. (dBm)	RoHS Compliant
TRSL-9110FG	2.125/1.063	200/100	10	1310	2X5 LC	0 to 70	-3 to -9.5	-21	Yes
TRSL-9110AFG	2.125/1.063	200/100	10	1310	2X5 LC	-40 to 85	-3 to -9.5	-21	Yes

Absolute Maximum Ratings					
Parameter	Symbol	Min	Max	Units	Notes
Storage Temperature	Tstg	-40	85	°C	
Operating Temperature	Topr	0 -40	70 85	°C	TRSL-9110FG TRSL-9110AFG
Soldering Temperature	---		260	°C	10 seconds on leads only
Power Supply Voltage	Vcc	0	4.5	V	
Input Voltage	---	GND	Vcc	V	
Output Current	Iout	0	30	mA	

Recommended Operating Conditions					
Parameter	Symbol	Min	Typ	Max	Units / Notes
Power Supply Voltage	Vcc	3.13	3.3	3.47	V
Operating Temperature	Topr	0 -40		70 85	°C / TRSL-9110FG °C / TRSL-9110AFG
Data Rate		1063	2125		Mb/s
Power Supply Current	Icc		200	250	mA

Transmitter Specifications (0°C < Topr < 70°C, 3.13V < Vcc < 3.47V)						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Optical						
Optical Transmit Power	Po	-9.5	---	-3	dBm	1
Output Center Wavelength	λ	1270	1310	1360	nm	
Output Spectrum Width	$\Delta\lambda$	---	---	2	nm	RMS (σ)
Extinction Ratio	E _R	9		---	dB	
Optical Modulation Amplitude (peak-to-peak)	OMA	174			μ W	FC-PI Standard
Optical Rise Time	t _r			160	ps	20% to 80% Values
Optical Fall Time	t _f			160	ps	20% to 80% Values
Relative Intensity Noise	RIN			-120	dB/Hz	
Electrical						
Data Input Current – Low	I _{IL}	-350			μ A	
Data Input Current – High	I _{IH}			350	μ A	
Differential Input Voltage	V _{IH} - V _{IL}	300			mV	
Data Input Voltage – Low	V _{IL} - V _{CC}	-2.0		-1.58	V	2
Data Input Voltage -- High	V _{IH} - V _{CC}	-1.1		-0.74	V	2
Disable Input Voltage -- Low	V _{TDISL}	0		0.5	V	TX Output Enabled
Disable Input Voltage -- High	V _{TDISH}	V _{CC} - 1.3		V _{CC}	V	TX Output Disabled
Shut Off Time for TxDis	t _{DIS}			1	ms	

 Notes: 1. Output power is power coupled into a 9/125 μ m MM fiber.

2. These inputs are compatible with 10K, 10KH and 100K ECL and PECL inputs.

Receiver Specifications (0°C < Topr < 70°C, 3.13V < Vcc < 3.47V)						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Optical						
Sensitivity @ 2.125 Gbps	Sen. 2X	---	---	-21	dBm	1
Sensitivity @ 1.063 Gbps	Sen. 1X	---	---	-22		1
Maximum Input Power	P _{in}	-3		---	dBm	
Signal Detect -- Asserted	P _a	---	---	-21	dBm	Transition: low to high
Signal Detect -- Deasserted	P _d	-35	---	---	dBm	Transition: high to low
Signal detect -- Hysteresis		1.0	---		dB	
Wavelength of Operation		1100	---	1600	nm	
Electrical						
Data Output Voltage – Low	V _{OL} - V _{CC}	-2.0		-1.58	V	2
Data Output Voltage – High	V _{OH} - V _{CC}	-1.1		-0.74	V	2
Signal Detect Output Voltage -- Low	V _{SDL}			0.5	V	
Signal Detect Output Voltage -- High	V _{SDH}	2.0			V	

 Notes: 1. Minimum sensitivity and saturation levels at BER=1E-12 for a 2⁻¹-1 PRBS.

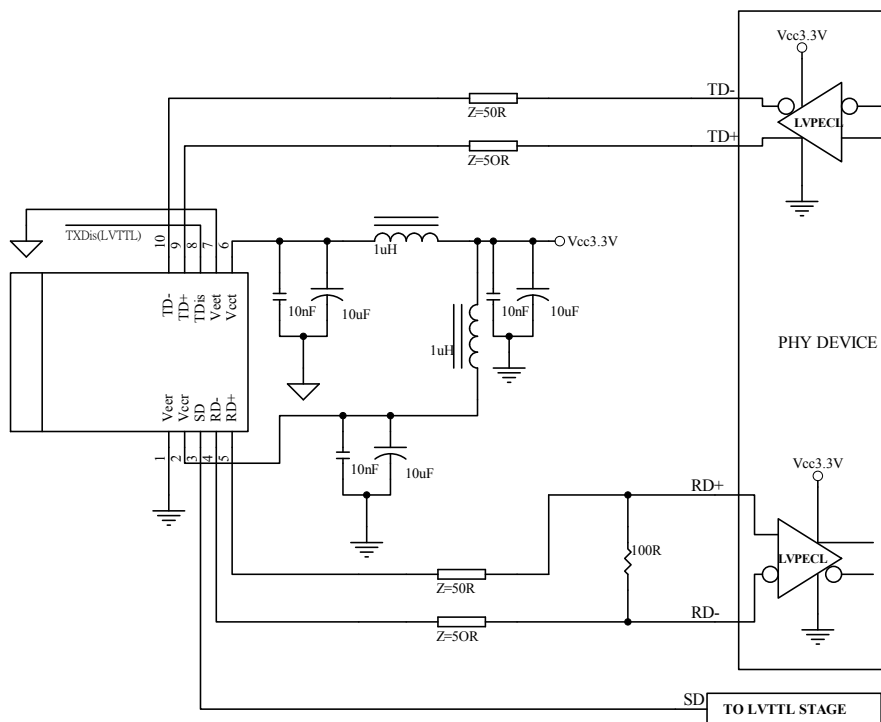
2. These outputs are compatible with 10K, 10KH and 100K ECL and PECL outputs.

CONNECTION DIAGRAM



PIN	Symbol	Notes
1	V_{EEt}	Directly connect this pin to the receiver ground plane
2	V_{CCr}	+3.3V dc power for the receiver section
3	SD	Active high on this indicates a received optical signal.
4	RD-	Receiver Dataout Bar. See recommended circuit schematic
5	RD+	Receiver Dataout. See recommended circuit schematic
6	V_{CCt}	+3.3V dc power for the transmitter section
7	V_{EEt}	Directly connect this pin to the transmitter ground plane
8	TDIs	Transmitter Disable. Connect this pin to +3.3V TTL logic "1" to disable module To enable module connect to TTL logic low "0"
9	TD+	Transmitter Data In. See recommended circuit schematic
10	TD-	Transmitter Data In Bar. See recommended circuit schematic
MS	MS	Mounting Studs. Connect to Chassis Ground

RECOMMENDED CIRCUIT SCHEMATIC

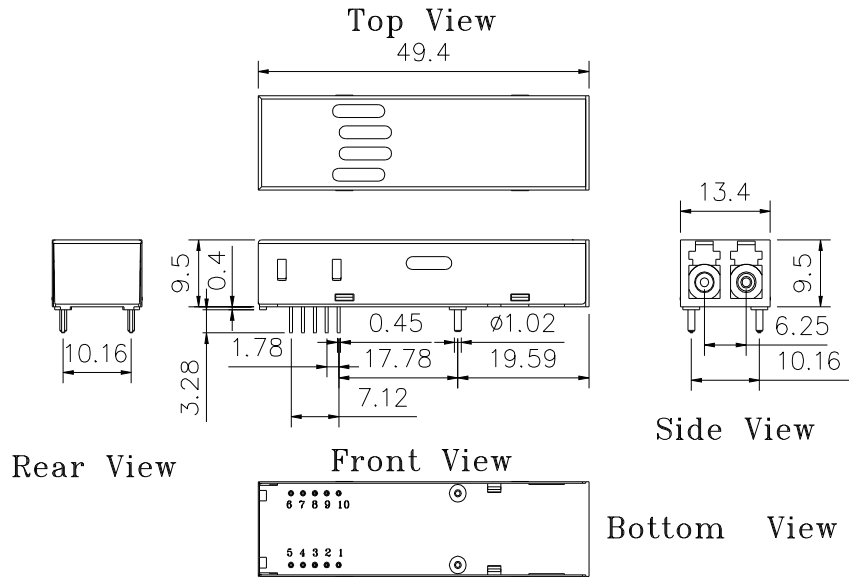


- Note: 1. TX input is terminated inside the module.
 2. Veer and Veet are not internally connected to each other.
 3. 50 Ω line pattern and component placements on TD+/TD- and RD+/RD- lines shall be symmetrical for better impedance matching.

PACKAGE DIAGRAM

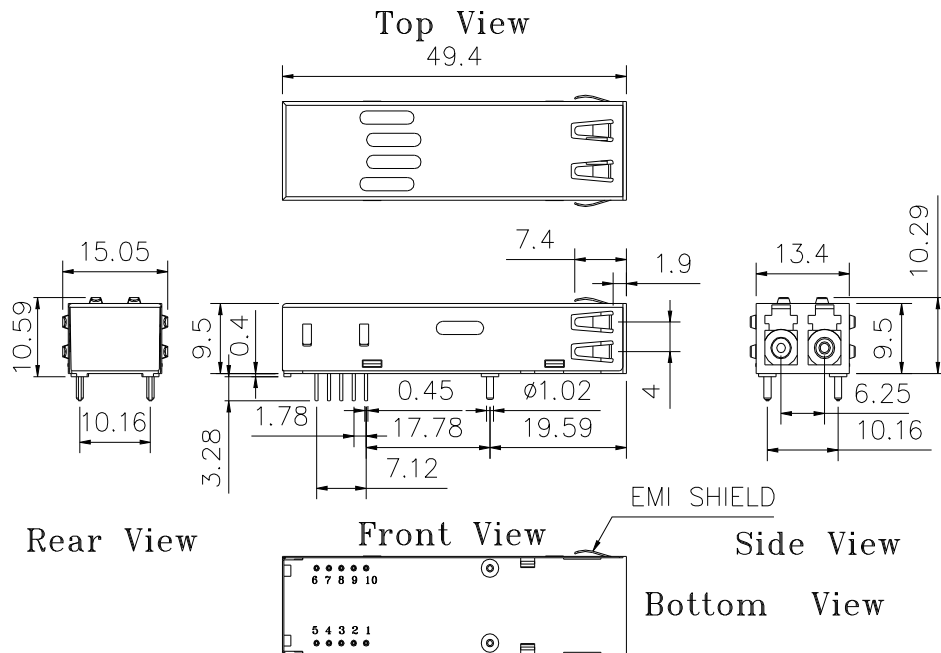
Units in mm

1) Standard Case



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2) Extended Case



TRSL-9110FEG / TRSL-9110AFEG

Note: Specifications subject to change without notice.

REVISION HISTORY

Version	Subject	Release Date
3.0	Revise Part Number of I-Temp Model	2015/6/2