

### XPS-23100WG

(RoHS Compliant)

### 10Gbps / 100km / 1550 nm XFP Single-Mode Optical Transceiver for 10GbE / 10G FC

#### FEATURES

- Support 9.95 Gbps to 10.5 Gbps
- Complaint with XFP MSA
- Compliant with XFI 10G Serial Electrical Interface
- **Maximum Link Length of 100 km**
- Temperature-stabilized 1550 nm EML Transmitter
- High Sensitivity APD Receiver
- 2-Wire Interface for Integrated Digital Diagnostic Monitoring
- No reference Clock required
- Power Dissipation < 3.5 W
- +3.3V and + 5V Power Supply
- RoHS Compliant
- -5 to 70°C Case Operating Temperature

#### APPLICATIONS

- 10GBASE-ZR/ZW
- 100 km 10G Fibre Channel

#### LASER SAFETY

This single-mode transceiver is a Class 1 laser product. It complies with IEC-60825 and FDA 21 CFR 1040.10 and 1040.11. The transceiver must be operated within the specified temperature and voltage limits. The optical ports of the module shall be terminated with an optical connector or with a dust plug.

#### DESCRIPTION

XPS-23100WG series transceivers are designed for single-mode fibre serial optical data communications such as 10G Ethernet 10GBASE-ZR/ZW and 10G Fibre Channel.

The transceiver consists of two sections: The transmitter section consists of a temperature-stabilized 1550 nm electrical-modulated laser (EML), driver and signal conditioner. The receiver section incorporates a Avalanche photodiode (APD) integrated with a trans-impedance preamplifier (TIA) and signal conditioner.

The module is with the XFP 30-pin connector to allow hot plug capability. Integrated Tx and Rx signal conditioners provide high jitter-tolerance for full XFI compliance and no external reference clock required. The internally ac coupled high speed serial I/O simplifies interfacing to external circuitry. +5V and +3.3V power supplies are needed. The optical output can be disabled by LVTTTL logic high-level input of TX\_DIS. Loss of signal (RX\_LOS) output is provided to indicate the loss of an input optical signal of receiver.

A serial EEPROM in the transceiver allows the user to access transceiver digital diagnostic monitoring and configuration data via the 2-wire XFP Management Interface. This interface uses a single address, A0h, with a memory map divided into a lower and upper area. Basic digital diagnostic data is held in the lower area while specific data is held in a series of tables in the high memory area.

#### ORDER INFORMATION

P/No.	Bit Rate (Gb/s)	10GBASE	Distance (km)	Wavelength (nm)	Package	Case Temp (°C)	RoHS Compliant
XPS-23100WG	9.95 – 10.5	ZR/ZW	100	1550 EML	XFP with DMI	-5 to 70	Yes

Absolute Maximum Ratings						
Parameter	Symbol	Min	Max	Units	Notes	
Storage Temperature	Tstg	-40	85	°C		
Operating Case Temperature	Topr	-5	70	°C		
Relative Humidity	RH	0	85	%	Non condensing	
Power Supply Voltage (5V)	Vcc5	-0.5	6.0	V		
Power Supply Voltage (3.3V)	Vcc3	-0.5	3.6	V		
Receiver Input Optical Power	Mip		-4	dBm	Received average power	

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Recommended Operating Conditions					
Parameter	Symbol	Min	Typ	Max	Units / Notes
Power Supply Voltage (5V)	V <sub>CC5</sub>	4.75	5	5.25	V
Power Supply Voltage (3.3V)	V <sub>CC3</sub>	3.13	3.3	3.47	V
Power Supply Current (@5V)	I <sub>CC5</sub>			500	mA / 1
Power Supply Current (@3.3V)	I <sub>CC3</sub>			750	mA / 1
Power Dissipation	P <sub>D</sub>			3.5	W
Operating Case Temperature	T <sub>opr</sub>	-5		70	°C
Data Rate		9.95		10.5	Gb/s

1. Including in rush current. Maximum module current ramp rate is 100 mA/μs.

Transmitter Optical Specifications (T <sub>opr</sub> = -5 to 70°C, V <sub>cc5</sub> =5V±5%, V <sub>cc3</sub> = 3.3V ±5%)						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Average Launch Power	P <sub>O, Avg</sub>	+1		+4	dBm	2
Extinction Ratio	ER	8.2			dB	3
Output Center Wavelength	λ <sub>c</sub>	1530		1565	nm	
Output Spectrum Width	σ <sub>λ</sub>			1	nm	-20 dB width
Side Mode Suppression Ratio	SMSR	30			dB	
Relative Intensity Noise	RIN			-130	dB/Hz	
Optical Eye Mask	Compliant with IEEE 802.3ae 10GBASE-L XFP MSA					
Average Launch Power of OFF Transmitter				-30	dBm	

2. Output power is power coupled into a 9/125 μm single-mode fiber.

3. ER=9 dB minimum for data rate 9.9 Gb/s to 10.7 Gb/s.

Receiver Optical Specifications (T <sub>opr</sub> = -5 to 70°C, V <sub>cc5</sub> =5V±5%, V <sub>cc3</sub> = 3.3V ±5%)						
Parameter	Symbol	Min	Typ	Max	Units	Notes
Sensitivity @10.3125 Gb/s	Sen1			-24	dBm	4,5
Receiver Overload	P <sub>MAX</sub>	-7	---		dBm	
LOS -- Deasserted	LOS <sub>D</sub>	---	---	-30	dBm	Transition: low to high
LOS -- Asserted	LOS <sub>A</sub>	-37	---	---	dBm	Transition: high to low
LOS -- Hysteresis		0.5	---		dB	
Wavelength of Operation	λ <sub>c</sub>	1270		1580	nm	
Optical Return Loss	ORL			-27	dB	

4. Measured at 1528 – 1565 nm with worst ER; BER<10<sup>-12</sup> and PRBS 2<sup>31</sup>-1.

5. Measured with worst ER; BER < 10<sup>-12</sup> and PRBS 2<sup>31</sup>-1.

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Electrical Characteristics						
Parameter	Symbol	Min	Typ	Max	Units	Notes
<b>High-Speed Signal (CML) Interface Specification</b>						
Input Data Rate			9.95328 10.3125		Gb/s	6 7
Input Date Rate Tolerance			±20 ±100		ppm	6 7
Differential Input Impedance	R <sub>in</sub>		100		Ω	
Differential Data Input Amplitude		120		820	mV <sub>pp</sub>	8, Internally AC coupled
Output Data Rate			9.95328 10.3125		Gb/s	6 7
Output Date Rate Tolerance			±100		ppm	6,7
Differential Output Impedance	R <sub>out</sub>		100		Ω	
Differential Data Output Amplitude		340		850	mV <sub>pp</sub>	8, Internally AC coupled
<b>Low-Speed Signal (LVTTTL) Interface Specification</b>						
Input High Voltage		2.0		V <sub>cc</sub>	V	
Input Low Voltage		GND		0.8	V	
Output High Voltage		2.4		V <sub>cc</sub>	V	
Output Low Voltage		GND		0.5	V	
<b>Reference Clock (LVPECL) Interface Specification</b>						
No reference clock						

6. 10GBASE-ZW.

7. 10GBASE-ZR.

8. The differential input and output amplitudes are per XFP MSA mask at points B' and C'.

Transceiver Timing Characteristics						
Parameter	Symbol	Min	Typ	Max	Units	Notes
TX_DIS Assert Time	t <sub>off</sub>			10	μs	
TX_DIS Negate Time	t <sub>on</sub>			2	ms	
Time to Initialize	t <sub>init</sub>			300	ms	
Interrupt Assert Delay	interrupt <sub>on</sub>			200	ms	
Interrupt Negate Delay	interrupt <sub>off</sub>			500	μs	
P_Down/PST Assert Delay	P_Down/RST <sub>on</sub>			100	μs	
P_Down Negate Delay	P_Down/RST <sub>off</sub>			300	ms	
Mod_NR Assert Delay	Mod_nr <sub>on</sub>			1	ms	
Mod_NR Negate Delay	Mod_nr <sub>off</sub>			1	ms	
Mod_Desel Assert Time	T_Mod_Desel			2	ms	
Mod_Desel De-Assert Time	T_Mod_Sel			2	ms	
P_Down Reset Time	T <sub>reset</sub>	10			μs	
RX_LOS Assert Delay	T_Los <sub>on</sub>			100	μs	
RX_LOS Negate Delay	T_Los <sub>off</sub>			100	μs	
Serial ID Clock Rate	f <sub>SCL</sub>	0		400	kHz	

### Management Interface

The structure of the memory map is shown in Figure 1, which is accessible over a 2-wire serial interface at the 8-bit address 1010000X (A0h). The normal 256 byte I2C address space is divided into low and upper blocks of 128 Bytes. The lower block of 128 Bytes is always directly available and is used for the diagnostics and control function. Multiple blocks of memories are available in the upper 128 Bytes of the address space. These are individually addressed through a table select Byte which the user enters into a location in the lower address space. Thus, there is a total available address space of 128\*256 = 32 Kbytes in this upper memory space. The contents of Table 01h are listed in Table 1 below. Please refer SFF INF-8077i (Revision 4.5) for detailed information.

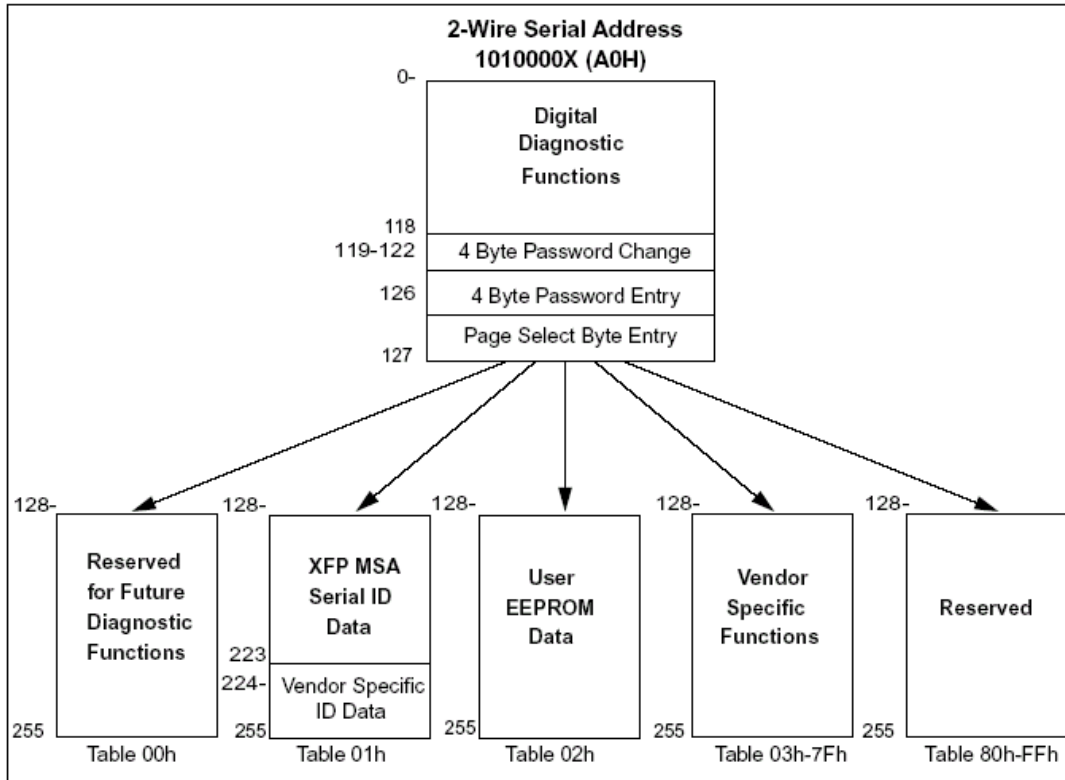
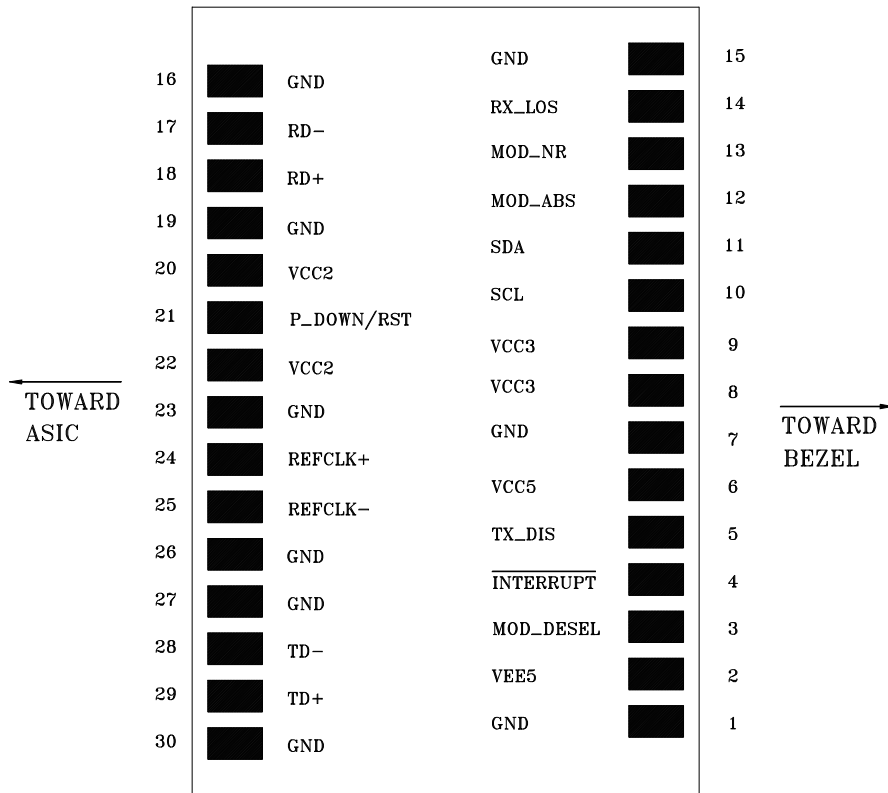


Figure 1. 2-wire Serial Digital Diagnostic Memory Map

**Table 1 Monitoring Specification**

Data Address	Parameter	Accuracy
96 ~ 97	Temperature	± 3°C
98 ~ 99	Reserved	
100 ~ 101	Tx Bias	± 10%
102 ~ 103	Tx Power	± 2dB
104 ~ 105	Rx Power	± 2dB
106 ~ 107	Vcc3	± 3%

### CONNECTION DIAGRAM



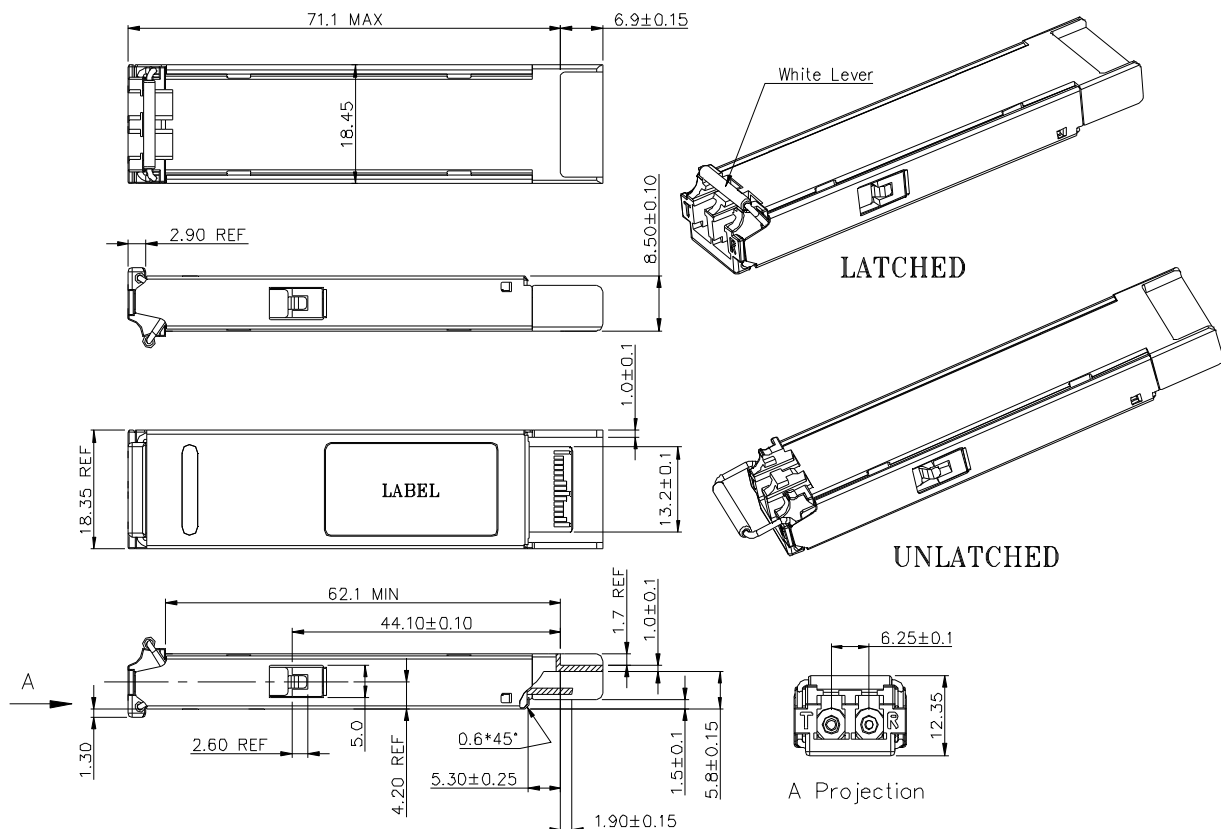
**Table 3 PIN Description**

PIN	Logic	Signal Name	Description	Note
1		GND	Module Ground	1
2		VEE5	-5.2V Power Supply <b>(Not required)</b>	3
3	LVTTL-I	Mod_Desel	Module De-select; When held low allows module to respond to 2-wire serial interface	
4	LVTTL-O	Interrupt	Interrupt; Indicates presence of an important condition which can be read over the 2-wire serial interface	2
5	LVTTL-I	TX_DIS	Transmitter Disable; Turns off transmitter laser output	
6		VCC5	+5V Power Supply	3
7		GND	Module Ground	1
8		VCC3	+3.3V Power Supply	
9		VCC3	+3.3V Power Supply	
10	LVTTL-I/O	SCL	Serial 2-wire interface clock	2
11	LVTTL-I/O	SDA	Serial 2-wire interface data line	2
12	LVTTL-O	Mod_Abs	Indicates Module is not present. Grounded in the Module	2
13	LVTTL-O	Mod_NR	Module Not Ready; Indicating Module Operational Fault	2
14	LVTTL-O	RX_LOS	Receiver Loss Of Signal Indicator	2
15		GND	Module Ground	1
16		GND	Module Ground	1
17	CML-O	RD-	Receiver Inverted Data Output	
18	CML-O	RD+	Receiver Non-Inverted Data Output	
19		GND	Module Ground	1
20		VCC2	+1.8V Power Supply <b>(Not required)</b>	3

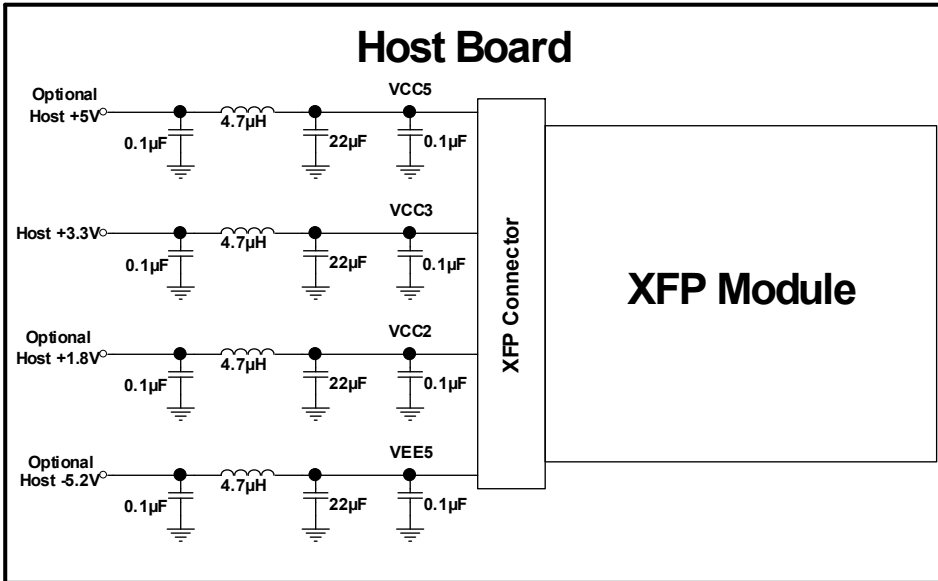
21	LVTTTL-I	P_Down/RST	Power down; When high, requires the module to limit power consumption to 1.5W or below. 2-Wire serial interface must be functional in the low power mode. Reset; The falling edge initiates a complete reset of the module including the 2-wire serial interface, equivalent to a power cycle.	
22		VCC2	+1.8V Power Supply ( <b>Not required</b> )	3
23		GND	Module Ground	1
24	PECL-I	RefCLK+	Reference Clock Non-Inverted Input, AC coupled on the host board. ( <b>Not used. Internally terminated to 50 ohm (100 ohm diff.)</b> )	4
25	PECL-I	RefCLK-	Reference Clock Inverted Input, AC coupled on the host board. ( <b>Not used. Internally terminated to 50 ohm (100 ohm diff.)</b> )	4
26		GND	Module Ground	1
27		GND	Module Ground	1
28	CML-I	TD-	Transmitter Inverted Data Input	
29	CML-I	TD+	Transmitter Non-Inverted Data Input	
30		GND	Module Ground	1

1. Module ground pins GND are isolated from the module case and chassis ground within the module.
2. Shall be pulled up with 4.7K-10Kohms to a voltage between 3.15V and 3.45V on the host board.
3. These PINs are open within module.
4. A Reference Clock input is not required. If present, it will be ignored.

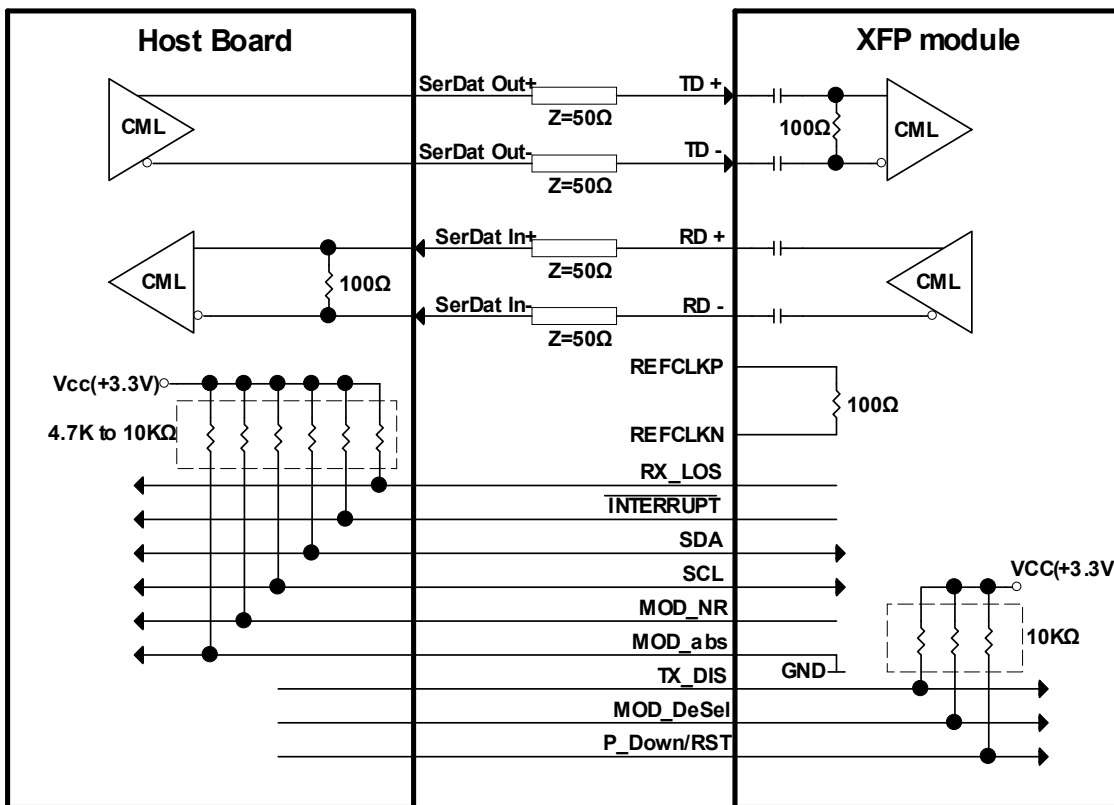
### MECHANICAL SPECIFICATION (UNITS IN MM)



## Recommended Power Circuit Schematic



## Recommended Interface Circuit



**Table 2 Serial ID Memory Contents (Table 01h)**

Address	Field Size (Byte)	Name of Filed	Description	Hex
128	1	Identifier	XFP	06
129	1	Ext. Identifier	power consumption < 3.5W, no Ref Clock required	90
130	1	Connector type	LC connector	07
131~138	8	Transceiver	10GBASE-ZR/ZW, 10GFC Extended Reach 1550nm, G959.1 P1L1-2D2	00 20 00 00 00 00 04 00
139	1	Encoding	64B/66B, NRZ	90
140	1	BR-Min	9.9 Gbps	63
141	1	BR-Max	10.5 Gbps	69
142	1	length (SMF)-Km	100 Km	64
143	1	Length (E-50μm)	0 m	00
144	1	Length (50 μm)	0 m	00
145	1	Length (62.5 μm)	0 m	00
146	1	Length (Copper)	0 m	00
147	1	Device Tech	1550 EML, APD detector	76
148~163	16	Vendor name	OPTOWAY	4F 50 54 4F 57 41 59 20 20 20 20 20 20 20 20 20
164	1	CDR Support	CDR supports 9.953G ~ 10.5G, XFI Loopback	E1
165~167	3	Vendor OUI		00 0E FA
168~183	16	Vendor PN	XPS-23100WG	58 50 53 2D 32 33 31 30 30 57 47 20 20 20 20 20
184~185	2	Vendor rev	ASCII ("31 61" means 1a revision)	xx xx
186~187	2	Wavelength	1547.5 nm	78 E6
188~189	2	Wavelength Tolerance	+/- 17.5 nm	0D AC
190	1	Max Case Temp	70deg	46
191	1	CC_BASE	Check sum of Byte 128 -- 190	
192~195	4	Power Supply	3.5 Wmax, 1.5W pd_max , 750mA 3.3V, 500 mA 5V	AF 96 A8 00
196~211	16	Vendor SN	ASCII	xx xx xx xx xx xx xx xx xx xx xx xx xx xx xx xx
212~219	8	Date code	ASCII Year (2 Byte), Month (2 Byte), Day (2 Byte)	xx xx xx xx xx xx 20 20
220	1	Diagnostic Monitoring Type	No BER Support, Average Power	08
221	1	Enhanced Options	Optional Soft TX_DISABLE implemented, Optional Soft P_down implemented	60
222	1	Aux Monitoring	AUX1 for Vcc3.	70
223	1	CC_EXT	Check sum of Byte 192 -- 222	
224~255	32	Vendor Specific		Reserved

Note: Specifications subject to change without notice.



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**REVISION HISTORY**

Version	Subject	Release Date
1.0	Initial datasheet	2009/8/1
2.0	Revise package diagram	2017/10/12